



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants:	Prashant Sethi, et al.	§	Group Art Unit:	2181
		§		
Serial No.:	10/806,787	§		
		§	Examiner:	Chun Kuan Lee
Filed:	March 22, 2004	§		
		§		
For:	A Method And System For Configuration	§	Atty. Dkt. No.:	ITL.1576US
	Of Processor Integrated Devices In Multi-	§		(P18870)
	Processor Systems	§		

Mail Stop Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLY TO OFFICE ACTION MAILED JULY 27, 2006

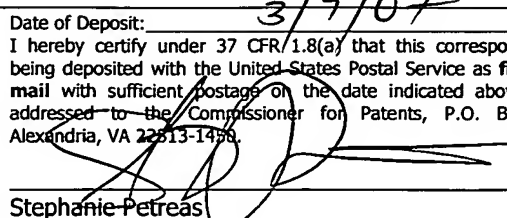
Sir:

In response to the Office Action mailed July 27, 2006, please amend the above-referenced patent application as follows:

Amendments to the Claims begin on page 2 of this paper.

Amendments to the Drawings begin on page 6 of this paper and includes an attached replacement sheet.

Remarks/Arguments begin on page 7 of this paper.

Date of Deposit: 3/9/07
I hereby certify under 37 CFR 1.8(a) that this correspondence is being deposited with the United States Postal Service as **first class mail** with sufficient postage on the date indicated above and is addressed to the Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450.

Stephanie Petreas